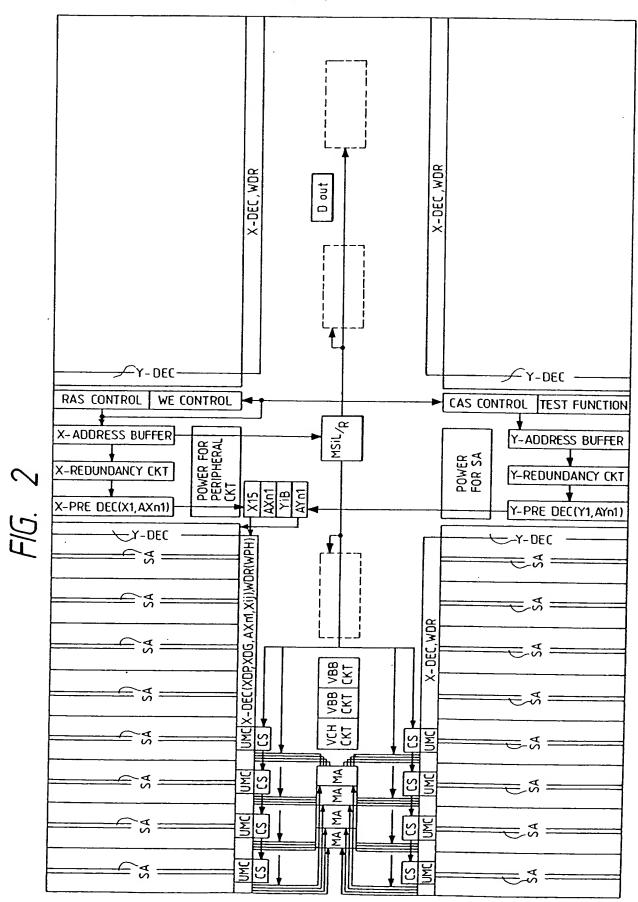
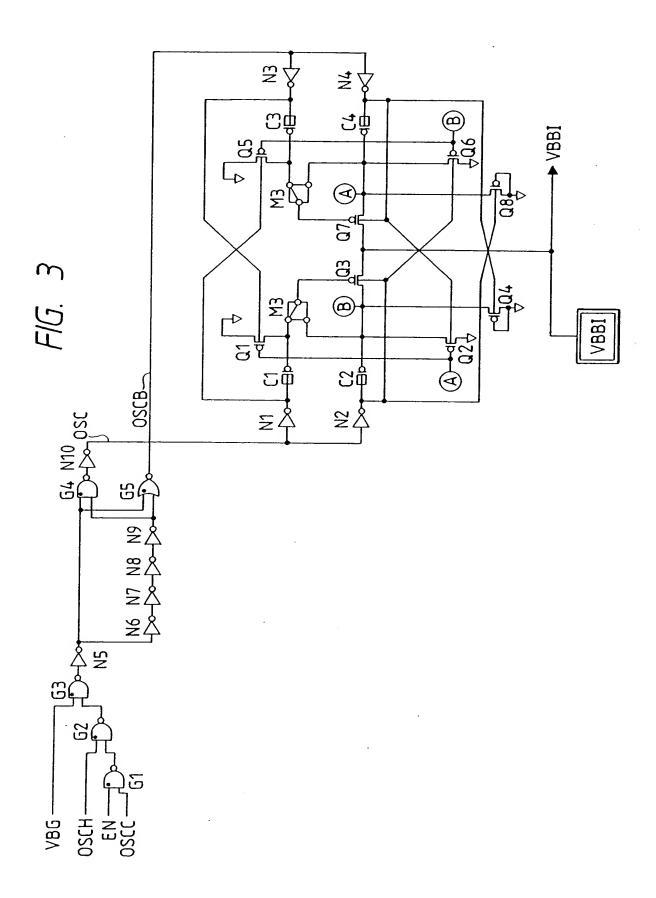
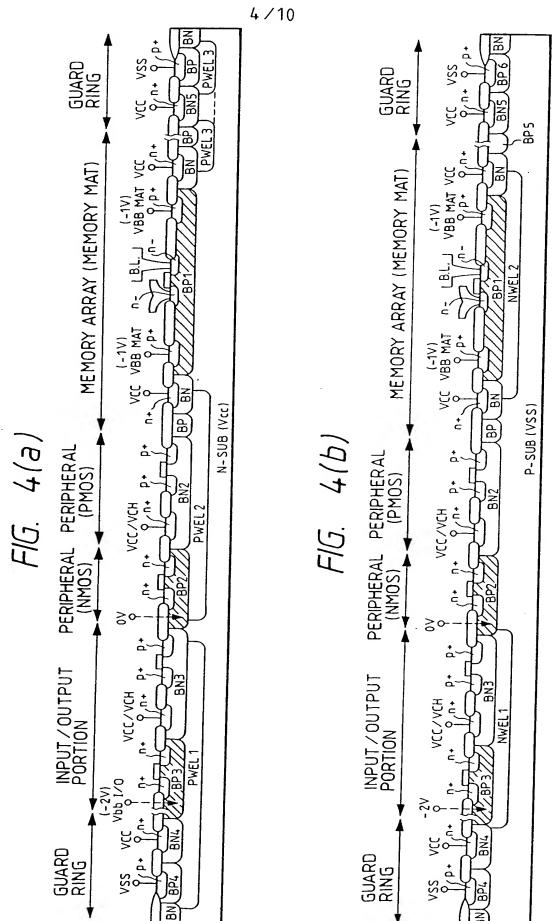
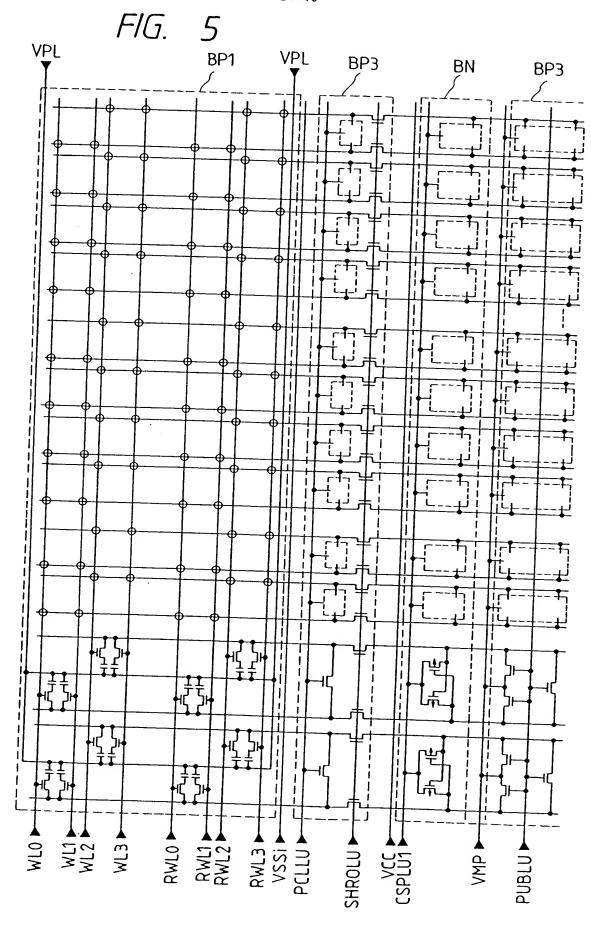


-Y-ADDRESS DRIVER (LOGIC STEP)









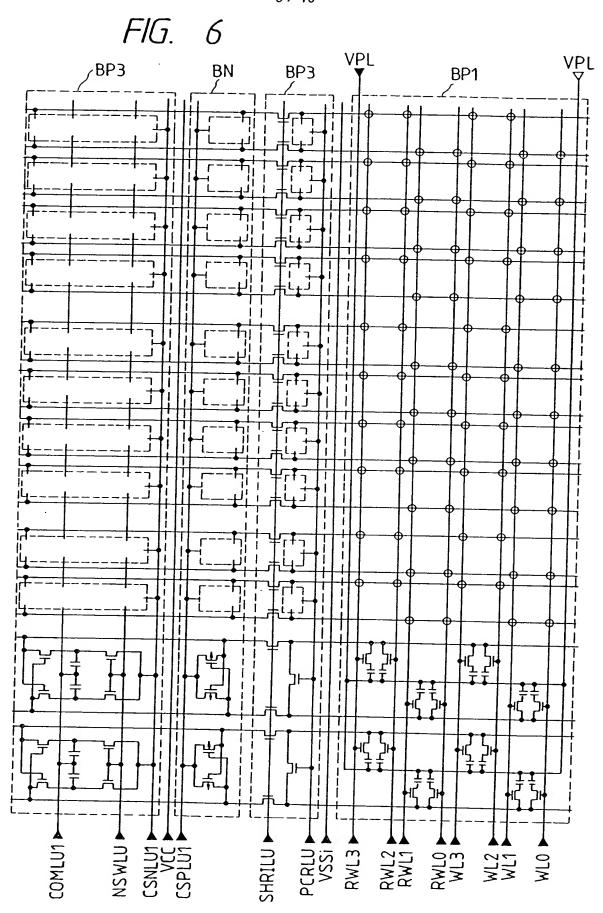


FIG. 7

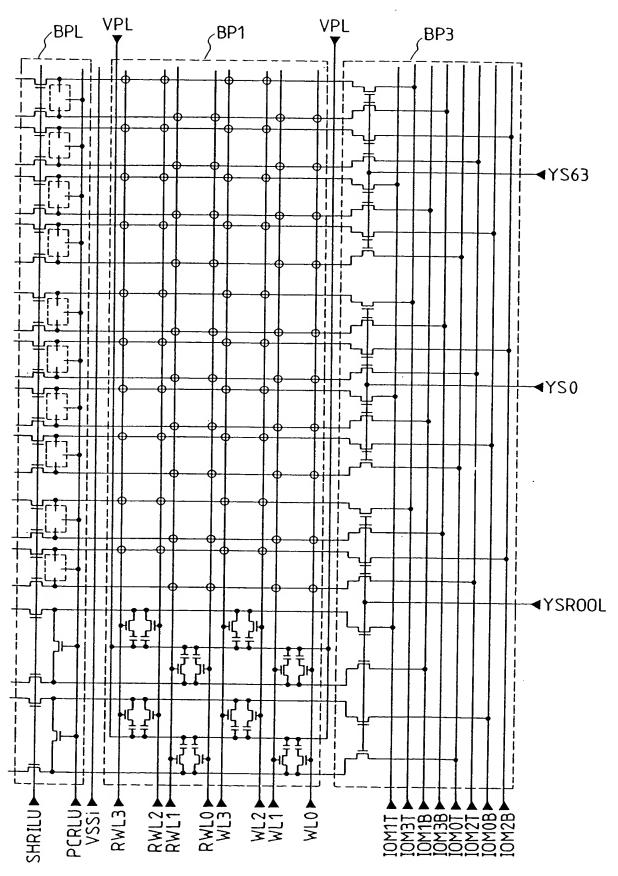
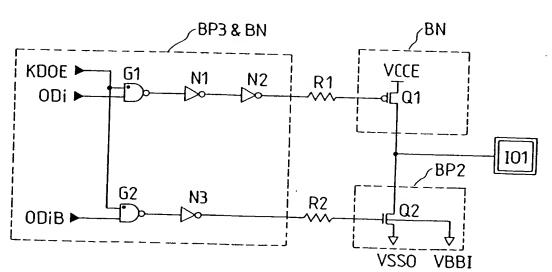


FIG. 8 WLi VCH VCH **WPH** VEH VS2 -DEC1 -DEC2 -DEC3 ÅN2 G2 4 N1 ⊸∞)G1 FIG. 9



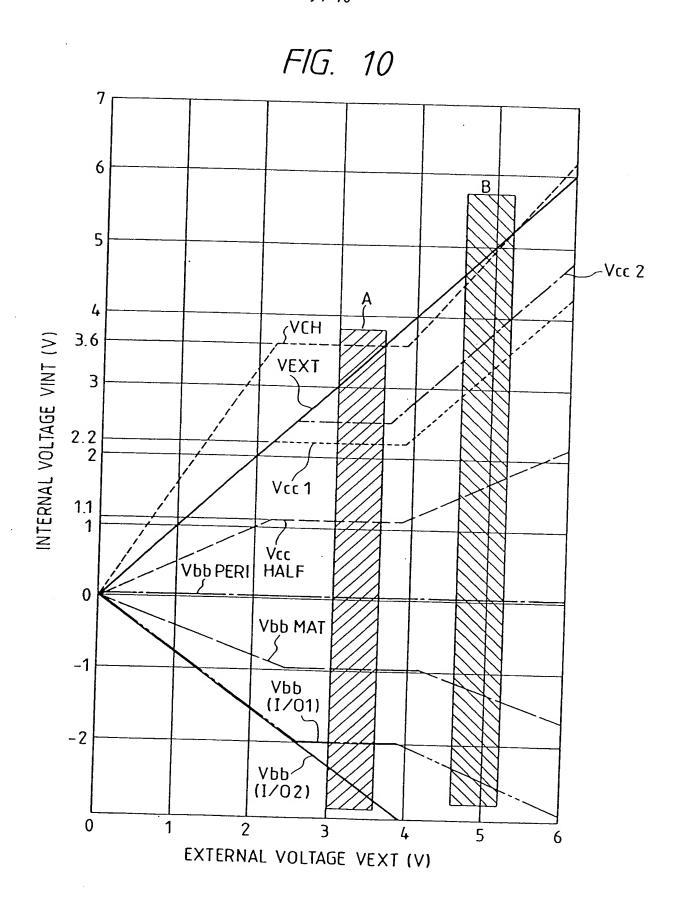


FIG. 11

